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## Summary

- Design, Product Development and NPI hands-on professional with diversified experience
- Effective engineering team leader, mentor and coach delivering and driving delivery of DFX quality solutions from cross-functional teams, often working on multiple parallel projects
- Results-oriented continuous improvement of operations and product development process for increased efficiency, quality assurance and cost reduction
- Latest experience in design, NPI, manufacturing and support of high complexity, high power/cooling electronics equipment – supercomputer class design verification reconfigurable system (KW's of power, liquid cooling with "hot water", high density interconnect, fiber optics, complex PCA)
- Past experience includes design and management in areas of computers/workstations, industrial control systems, graphics/video processing hardware, special communication subsystems, and more

**2013** Patent for "Methods and System for preventing contamination in fiber optic connectors"

**2010** Cadence "Excellence in Innovation" Enterprise Innovation Award in recognition of innovating contribution to Cadence Design Systems

**2004** Cadence "Quality Award" for contribution to Stage-Gate documentation for Hardware Development process represented HW development and manufacturing divisions of this corporate-wide initiative

Additional information at: <http://davidbakhmutsky.com> <http://www.linkedin.com>

## Work Experience

RSM **2014 – present**

Senior Teacher

- Teaching High School math curriculum

DMB Technologies **2013 - present**

Consultant

- Offering quality DFX solutions in development of complex systems with HW configurable options

Cadence Design Systems **2011 - 2013**

Director, HW Product Development and Product Engineering

- Managed Design of Power, Packaging, Cooling, Compliance, Interconnect, ID with focused responsibilities for system DFX and contributor as a HW System Architecture team member
- Managed Operational Engineering, including: NPI, Documentation Control, Component and Test Engineering

Cadence Design Systems **1999 - 2011**

Product Engineering Manager

- Designed and managed design/re-design of board and subsystems of multiple high complexity products
- Managed PE, NPI, Test Engineering, Compliance, QA, including SW for Diagnostics, Test, Configuration and Embedded Control

Quickturn Design Systems

**1996 - 1999**

Product Engineering Manager

- Created and Managed PE Group
- Contributed as EE, Compliance Engineer & System Designer, Technical Writer, ASIC PE
- Reduced RMA inventory by 70%, reduced WIP inventory by \$2 MLN, increased product reliability by 200%

**LANGUAGES**

English, Russian, Latvian

**EDUCATION**

[equivalent of] MS Physics, Latvian State University